Labelled Markov Processes Lecture 1: Labelled Transition Systems

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Outline



- 2 Labelled transition systems
- Bisimulation and Coinduction
- Hennessy-Milner Logic

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Panangaden Labelled Markov Processes

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Bisimulation and Coinduction



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Overview

• Lecture 1: Labelled transition systems and bisimulation.

- Lecture 2: Labelled Markov processes.
- Lecture 3: Logical characterization of bisimulation.
- Lecture 4: The metric analogue of bisimulation.

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This lecture

• Labelled transition systems.

Bisimulation.

- Making sense of coinduction.
- Games for bisimulation and simulation.
- Logical characterization.

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Summary of Results

- Probabilistic bisimulation can be defined for continuous state-space systems. [LICS97]
- Logical characterization. [LICS98,Info and Comp 2002]
- Metric analogue of bisimulation. [CONCUR99, TCS2004]
- Approximation of LMPs. [LICS00, Info and Comp 2003]
- Weak bisimulation. [LICS02,CONCUR02]
- Real time. [QEST 2004, JLAP 2003,LMCS 2006]

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- Abbas Edalat
- Vincent Danos

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The definition

A set of states S,

- a set of *labels* or *actions*, L or A and
- a transition relation $\subseteq S \times A \times S$, usually written

$$\rightarrow_a \subseteq \mathbf{S} \times \mathbf{S}.$$

The transitions could be indeterminate (nondeterministic). • We write $s \xrightarrow{a} s'$ for $(s, s') \in \rightarrow_a$.

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A simple example



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A vending machine



Panangaden Labelled Markov Processes

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Vending machine LTSs



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Another (?) vending machine LTSs



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Are the two LTSs equivalent?

• One gives *us* the choice whereas the other makes the choice *internally*.

- The sequences that the machines can perform are identical: [Rs.5; (Cof + Tea); Cup]*
- We need to go beyond language equivalence.

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Bisimulation

s and *t* are states of a labelled transition system. We say *s* is **bisimilar** to *t* – written $s \sim t$ – if

$$s \stackrel{a}{\longrightarrow} s' \Rightarrow \exists t'$$
 such that $t \stackrel{a}{\longrightarrow} t'$ and $s' \sim t'$

and

$$t \xrightarrow{a} t' \Rightarrow \exists s' \text{ such that } s \xrightarrow{a} s' \text{ and } s' \sim t'.$$

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Does it make sense?

• The definition of bisimilarity seems circular.

- In fact, it is perfectly well defined.
- There are three or four ways of explaining it.

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Coinduction via induction

- Define a *family* of equivalence relations ∼_n indexed by the natural numbers.
- \sim_0 is the universal relation: $\forall s, t \ s \sim_0 t$.
- $s \sim_{n+1} t$ if

$$\forall a, s \xrightarrow{a} s' \Rightarrow \exists t', t \xrightarrow{a} t' \text{ and } s' \sim_n t'$$

and vice versa.

• $s \sim t$ if and only if $\forall n, s \sim_n t$.

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Coinduction as a greatest fixed point

• Fix a labelled transition system with state space S.

- Let \mathcal{R} be the collection of equivalence relations on S ordered by inclusion.
- Define $\mathcal{F} : \mathcal{R} \longrightarrow \mathcal{R}$ by

 $s\mathcal{F}(R)t$ means $\forall a, s \xrightarrow{a} s' \Rightarrow \exists t', t \xrightarrow{a} t'$ and s'Rt'

and vice versa.

- *R* is a complete lattice partially ordered by inclusion and *F* is a monotone function.
- It is a (moderately) easy exercise to show that *F* has a greatest fixed point: this is bisimulation.

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Bisimulation relations

• Define *a* (note the indefinite article) bisimulation relation *R* to be an equivalence relation on *S* such that

sRt means $\forall a, s \xrightarrow{a} s' \Rightarrow \exists t', t \xrightarrow{a} t'$ with s'Rt'

and vice versa.

- This is not circular; it is a condition on *R*.
- We define s ~ t if there is *some* bisimulation relation R with *sRt*.
- This is the version that is used most often.

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An example



Here s₀ and t₀ are not bisimilar.

• However *s*₀ and *t*₀ can simulate each other!

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The bisimulation game

- Two players: maker (M) and spoiler (S). M wants to establish a bisimulation and S wants to spoil the bisimulation.
- S chooses a process with which to play and makes a move.
- M must match S's move.
- S chooses again which process she wants to play and makes a move which M must match.
- If M has a winning strategy then the processes are bisimilar.
- If we did not allow S to switch after the first move then a winning strategy for M implies two-way simulation: much weaker than bisimulation.

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How do we know that two processes are **not** bisimilar?

Define a logic as follows:

 $\phi ::== \mathsf{T} |\neg \phi| \phi_1 \wedge \phi_2 | \langle \mathbf{a} \rangle \phi$

- $s \models \langle a \rangle \phi$ means that $s \xrightarrow{a} s'$ and $t \models \phi$.
- We can define a dual to $\langle \rangle$ (written []) by using negation.
- s |= [a]φ means that if s can do an a the resulting state must satisfy φ.

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Examples of HM Logic

- *T* is satisfied by any process, *F* is not satisfied by any process.
- $s \models \langle a \rangle T$ means *s* can do an *a* action.
- $s \models \neg \langle a \rangle \phi$ or $s \models [a]F$ means s cannot do an *a* action.
- $s \models \langle a \rangle (\langle b \rangle T)$ means that s can do an *a* and then do a *b*.

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The Hennessy-Milner theorem

- Two processes are bisimilar if and only if they satisfy the same formulas of HM logic.
- Basic assumption: the processes are finitely-branching (otherwise you need infinitary conjunctions).
- To show that two processes are not bisimilar find a formula on which they disagree.
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Our first example



• Here s_0 and t_0 are not bisimilar.

• $s_0 \models \langle a \rangle (\neg \langle b \rangle T)$ but t_0 does not satisfy this formula.

- $t_0 \models \langle a \rangle (\langle b \rangle T \land \langle c \rangle T)$ but s_0 does not satisfy this.
- The conjunction captures branching structure.

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The role of negation

• Consider the processes below:



- $s_0 \models \langle a \rangle \neg \langle b \rangle T$ but t_0 does not.
- s_0 and t_0 agree on all formulas without negation.
- Note that [a] has an implicit negation.

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Sac

The role of negation

• Consider the processes below:



- $s_0 \models \langle a \rangle \neg \langle b \rangle T$ but t_0 does not.
- s_0 and t_0 agree on all formulas without negation.
- Note that [a] has an implicit negation.

Sac

Simulation

- Simulation can be defined by dropping the "vice versas" in the definition of bisimulation.
- We would like a theorem of the form: if *s* simulates *t* then every formula that *t* satisfies is also satisfied by *s*.
- There cannot be a logical characterization of simulation as long as there is negation.

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SQC+

Next Lecture

We do everything probabilistically.

Panangaden Labelled Markov Processes

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